

**School of CET**

**System Software and Compiler lab**

**Assignment No.3**

**TY BTech CSE**

**Assignment Title:** Design of Pass 1 of Two Pass Macroprocessor.

**Aim:** Design suitable data structure & implement pass 1 of Two Pass Macroprocessor.

**Objective:** Design suitable data structure & implement pass 1 of Two Pass Macroprocessor. Input should consist of a one macro definition and one macro call and few assembly language instructions.

**Theory:**

Write about

1. Description about the macroprocessor.
2. Data structures required for 2 pass macroprocessor.
3. Flowchart for Pass I.
4. Algorithm for Pass 1

**Input:** Assembly Language Program.

**Output:**

1. Program without Macro Definition (Pass-I)

2. Macro Definition Table (MDT)

|  |  |
| --- | --- |
| Index | MDT- Instruction |
|  |  |

3. Macro Name Table (MNT)

|  |  |  |
| --- | --- | --- |
| Index | Macro Name | MDT- Index |
|  |  |  |

4. Argument List Array (ALA).

|  |  |
| --- | --- |
| Index | Dummy Argument |
|  |  |

**Conclusion:** The function of Pass 1 in a Macro Processor studied.

**Platform:** Linux (Java)

**Conclusion:** The function of Pass 1 in assembler is studied along with errors coming in each pass.

**Platform:** Linux (JAVA)